IN THE CLAIMS:

Please amend claims 1 and 11 as follows. A detailed listing of all claims is as follows.

Claim 1 (Currently Amended): A method of driving a liquid crystal display, comprising:

determining a ~~standard~~ data for driving video data normally;

determining an output data <u>for displaying</u> ~~displayed~~ on the liquid crystal display;

acquiring a modulation data by calculating differences between the ~~standard~~ <u>normal</u> data and the output data; and

~~setting~~ <u>storing</u> the modulation data in the liquid crystal display[[;]].

Claim 2 (Previously Presented): The method according to claim 1, wherein the modulation data includes an absolute value of the differences.

Claim 3 (Previously Presented): The method according to claim 1, further comprising steps of:

receiving an input data;

delaying the input data;

modulating the input data using the modulation data in accordance with the input data and the delayed input data;

adding the modulated data to the input data;

subtracting the modulated data from the input data;

comparing the delayed input data with the input data; and

selecting one of the added data and the subtracted data depending on the compared result.

Claim 4 (Previously Presented): A method of driving a liquid crystal display,

comprising steps of:

setting a modulation data in the liquid crystal display;

receiving an input data;

delaying the input data;

modulating the input data using the modulation data in accordance with the input data

and the delayed input data;

adding the modulated data to the input data;

subtracting the modulated data from the input data;

comparing the delayed input data with the input data; and

selecting one of the added data and the subtracted data depending on the compared result.

Claim 5 (Previously Presented): A method of driving a liquid crystal display,

comprising steps of:

receiving an input data;

dividing the input data into most significant bits and least significant bits;

delaying the most significant bits for a frame period;

modulating the most significant bits using the modulation data in accordance with the

most significant bits and the delayed most significant bits;

adding the modulated data to the non-delayed most significant bits;

subtracting the modulated data from the non-delayed most significant bits;

comparing the delayed most significant bits with the non-delayed most significant bits;
and

selecting one of the added data and the subtracted data depending on the compared result.

Claim 6 (Previously Presented): The method according to claim 5, further comprising:

combining the selected data with the least significant bits.

Claim 7 (Previously Presented): The method according to claim 1, further comprising

steps of:

receiving an input data;

dividing the input data into most significant bits and least significant bits;

delaying the most significant bits for a frame period;

modulating the most significant bits using the modulation data in accordance with the

most significant bits and the delayed most significant bits;

adding the modulated data to the non-delayed most significant bits;

subtracting the modulated data from the non-delayed most significant bits;

comparing the delayed most significant bits with the non-delayed most significant bits;

and

selecting one of the added data and the subtracted data depending on the compared result.

Claim 8 (Canceled).

Claim 9 (Previously Presented): The method according to claim 7, further comprising:

combining the selected data with the least significant bits.

Claim 10 (Canceled).

Claim 11 (Currently Amended): A driving apparatus for a liquid crystal display,

comprising:

a modulator ~~having~~ storing a modulation data predetermined by calculating differences

between a ~~standard~~ data for driving video data normally and an output data for displaying

~~displayed~~ on the liquid crystal display.

Claim 12 (Previously Presented): The driving apparatus according to claim 11, wherein

the modulation data includes an absolute value of the differences.

Claim 13 (Previously Presented): The driving apparatus according to claim 11, further

comprising:

an input line receiving an input data;

a frame memory delaying the input data;

an adder adding a modulated data acquired by the modulator using the input data and the

delayed input data to the input data;

a subtracter subtracting the modulated data from the input data;

a comparator comparing the input data with the delayed input data for a frame period;

and

a selector selecting one of the added data and the subtracted data depending on the
compared result from the comparator.

Claim 14 (Previously Presented): A driving apparatus for a liquid crystal display,
comprising:

an input line receiving an input data;

a frame memory delaying the input data;

a modulator modulating the input data using the delayed input data;

an adder adding the modulated data to the input data;

a subtracter subtracting the modulated data from the input data;

a comparator comparing the input data with the delayed input data for a frame period;
and

a selector selecting one of the added data and the subtracted data depending on the
compared result from the comparator.

Claim 15 (Previously Presented): A driving apparatus for a liquid crystal display,
comprising:

an input line receiving an input data;

a frame memory delaying most significant bits of the input data;

a modulator modulating the most significant bits using the delayed most significant bits
and the non-delayed most significant bits;

an adder adding the modulated data to the non-delayed most significant bits;

a subtracter subtracting the modulated data from the non-delayed most significant bits;

a comparator comparing the non-delayed most significant bits with the delayed most significant bits; and

a selector selecting one of the added data and the subtracted data depending on the compared result from the comparator.

Claim 16 (Previously Presented): The driving apparatus according to claim 15, further comprising a combiner combining the selected data with the input data.

Claim 17 (Previously Presented): The driving apparatus according to claim 11, further comprising:

an input line receiving an input data;

a frame memory delaying most significant bits of the input data;

an adder adding a modulated data acquired by the modulator using the non-delayed most significant bits and the delayed most significant bits to the non-delayed most significant bits;

a subtracter subtracting the modulated data from the non-delayed most significant bits;

a comparator comparing the delayed most significant bits with the non-delayed most significant bits; and

a selector selecting one of the added data and the subtracted data depending on the compared result.

Claim 18 (Canceled).

Claim 19 (Previously Presented): The driving apparatus according to claim 17, further comprising a combiner combining the selected data with the input data.

Claim 20 (Canceled).